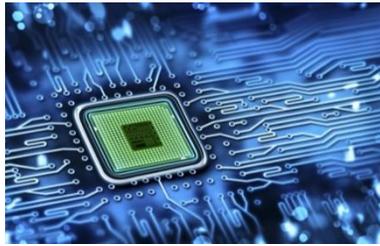




PASSION. PRECISION. PURITY.



## INTERNSHIP / MASTER THESIS

# The Magic Gate for Semiconductor Fabs

Are you looking for an opportunity to write your thesis in an industrial environment or obtain valuable industry experience, during or after your technical education? Here is one of many interesting topics we have on offer. We are also very open to your own ideas in order to create a matching opportunity for you at VAT.

Innovation has always been the driving force at VAT since the company was founded over 50 years ago. This has made us the world leader in vacuum valves and vacuum sealing technology. This pioneering spirit motivates us daily to meet our customers' requirements with enthusiasm. Together with our employees we stand for passion, innovation and quality. VAT is headquartered in Haag (Switzerland) and employs approximately 2 000 people worldwide. It has production centers in Haag (Switzerland), Penang (Malaysia) and Arad (Romania) as well as a production facility in Xinwu (Taiwan). With our customers mainly being situated in the United States and Asia, this provides a great opportunity to start an international career.

### What you will explore:

With the introduction of latest technologies and subsequent increasing market requirements in microchip manufacturing lines in terms of process cleanliness, process stability and low particle generation, it is essential that the manufacturing process of semiconductor components follows the highest level of purity and cleaning. You will be responsible to create the optimized manufacturing flow within VAT to reduce at the minimum the risk of contamination and to achieve the cleanest possible product. You will follow all production steps from raw materials to final product cleaning to identify the areas where optimization is required. Together with VAT's scientists, you will implement trials to tune and optimize the process parameters to achieve a Magic Gate which fulfills the most stringent cleaning and particles requirements. You will be part of a team which includes engineers, material scientists, chemists and physicists which will support you and supervise you during your experience.

### What we offer:

- Practical experience in the growing semiconductor industry and in a global environment
- Support by and coordination with experienced scientists
- Content & scope of this thesis may be discussed and defined in further details

### What you will need:

- Chemical / Material / Process engineering background
- Innovative and structured approach
- Ability in project planning and execution
- Willingness to learn and apply new techniques
- Proactivity

### Are You Ready for the Challenge?

Then we look forward to receiving your **electronic application sent to Robert Spasov or Stefano Caimi.**

E-Mail: [r.spasov@vat.ch](mailto:r.spasov@vat.ch) or [s.caimi@vat.ch](mailto:s.caimi@vat.ch)